

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-3 (canceled)

[Handwritten: Canceled]

1 Claim 4. (Previously Presented) A portable computer system comprising:
2 an indicating device having a plurality of light emitting devices activated according to a
3 signal from the portable computer system; and
4 a controller managing said indicating device to display power-on self-test (POST) codes in
5 response to operating states of the portable computer system, the power-on self-test codes being
6 generated in power-on self-test process by a basic input-output system (BIOS) of the computer
7 system,
8 with the operating states comprising of a power on or off state, number lock state, a capital
9 letter state, a scroll lock state, an access state of a disk drive, and a charge state of the battery.

1 Claim 5. (Original) The portable computer system of claim 4, with said indicating device
2 being a plurality of light emitting diodes, with each power-on self-test code corresponding to a
3 specific light emitting diode on the portable computer.

et publi →
1 Claim 6. (Original) The portable computer system of claim 5, with said light emitting diodes
2 sequentially aligned along a surface of the portable computer according to an order of operating
3 states being tested by the portable computer, the alignment accommodating a rapid view of a
4 progress of the power-on self-test.

1 Claim 7. (Original) The portable computer system of claim 6, with the light emitting diodes
2 indicating where an error has occurred in the portable computer system.

Claim 8. (Canceled)

sub P1 →
1 Claim 9. (Original) A portable computer system, comprising:
2 an address decoder coupled to an address bus generating a latch control signal by decoding
3 an address of an output port accommodating power-on self-test codes;
4 a latch coupled to a data bus of the portable computer system latching the power-on self-test
5 codes from the data bus in response to the latch control signal;
6 an indicating device having a plurality of lighting devices indicating operating states of the
7 portable computer system;
8 a controller generating an indicating control signal in response to the operating state; and
9 a selector sending either the indicating control signal or power-on self-test codes of the latch
10 to said indicating device.

2
3
1
2
3

Claim 10. (Original) The portable computer system of claim 9, with the latched power-on self-test codes being outputted to the indicating device when the address decoder translates the address of the output port for power-on self-test codes.

1
2
3

Claim 11. (Original) The portable computer system of claim 9, with the controller managing the selector to output the power-on self-test codes latched in the latch during the power-on self-test process.

1
2
3
4

Claim 12. (Original) The portable computer system of claim 11, further comprising a key input device coupled to the controller, said controller regulating the selector to output the power-on self-test codes held temporarily until a key input signal response from the key input device during the power-on self-test process.

1
2

Claim 13. (Original) The portable computer system of claim 12, with the key input device being a keyboard of the portable computer system.

1
2

Claim 14. (Original) The portable computer system of claim 13, with said selector being a multiplexer, the output of said multiplexer being controlled by the controller.


1

Claim 15. (Previously Presented) The portable computer of claim 14, with the lighting

pub DI
CT 2 devices being a plurality of light emitting diodes displaying the power-on self-test codes in
3 accordance with an order of the power-on self-test process.

Claims 16-18 (Canceled)

Pub DI
Claim 19. (Previously Presented) A method of displaying power-on self-test codes in a
2 portable computer system, comprising the steps of:
3 starting a power-on self-test process;
4 generating power-on self-test codes;
5 outputting the power-on self-test codes to a microprocessor to display the power-on self-test
6 codes;
7 testing each one of the elements of the portable computer system corresponding to the
8 respective power-on self-test codes;
9 determining whether the test is performed in safety;
10 completing the power-on self-test process if the test is performed in safety in all of the
11 elements; and
12 interrupting the power-on self-test process if the test is not performed in safety in any
13 element,
14 said step of displaying the power-on self-test codes further comprising:
15 receiving data through a predetermined input-output port of said microprocessor of the
16 portable computer;

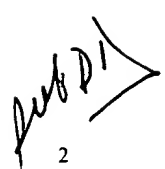
Cut pub 21 

18 generating an internal interrupt when the data is inputted to said microprocessor;
19 checking whether the data inputted through the predetermined input-output port of the
20 microprocessor is a power-on self-test code;
21 displaying the power-on self-test code through an indicator when data inputted is a power-on
22 self-test code; and
23 executing other interrupt routines when data is not a power-on self-test code.

1 Claim 20. (Original) The method of claim 19, with said indicator being a plurality of light
2 emitting diodes, with each power-on self-test code corresponding to a specific light emitting diode
3 on the portable computer.

1 Claim 21. (Original) The method of claim 20, with said light emitting diodes sequentially
2 aligned along a surface of the portable computer according to an order of operating states being
3 tested by the portable computer, the alignment accommodating a rapid view of a progress of the
4 power-on self-test.

Claims 22-23 (Canceled)

pub 21 

2 Claim 24. (Previously Presented) A computer, comprising:
3 an indicating device having a plurality of light emitting devices activated according to a
4 signal from the computer; and

Pub D1
C1

4 a controller managing said indicating device to display power-on self-test codes in response
5 to operating states of the computer, the power-on self-test codes being generated in power-on self-
6 test process by a basic input-output system of the computer system,
7 with the operating states comprising of a power on or off state, number lock state, a capital
8 letter state, a scroll lock state, an access state of a disk drive, and a charge state of the battery.

1 Claim 25. (Previously Presented) A computer, comprising:
2 an address decoder coupled to an address bus generating a latch control signal by decoding
3 an address of an output port accommodating power-on self-test codes;
4 a latch coupled to a data bus of the computer latching the power-on self-test codes from the
5 data bus in response to the latch control signal;
6 an indicating device having a plurality of lighting devices indicating operating states of the
7 computer;
8 a controller generating an indicating control signal in response to the operating state; and
9 a selector sending either the indicating control signal or power-on self-test codes of the latch
10 to said indicating device.